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TRANSMITTAL FORM <i>(to be used for all correspondence during pendency of filed application)</i>	Application Number	10/749,912	
	Filing Date	December 30, 2003	
	First Named Inventor	Donald E. Steiss	
	Group Art Unit Number	2186	
	Examiner Name	Not yet known	
Total Number of Pages in This Submission	2	Attorney Docket Number	22347-08261

ENCLOSURES (check all that apply)	
<input type="checkbox"/> Fee Transmittal Form (in duplicate) <input type="checkbox"/> Check Enclosed	<input type="checkbox"/> Issue Fee Transmittal
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<input checked="" type="checkbox"/> Status Request	<input type="checkbox"/>
<input type="checkbox"/> Revocation and Substitute Power of Attorney	<input type="checkbox"/>
REMARKS:	

SIGNATURE OF ATTORNEY OR AGENT		
Signature:	<i>Dorian Cartwright</i>	
Attorney/Reg. No.:	Dorian Cartwright, Reg. No. 52,853	Dated: 1/19/06

CERTIFICATE OF FACSIMILE TRANSMISSION		
I hereby certify that this correspondence, including the enclosures identified above, is being transmitted on the date shown below via facsimile to: Commissioner for Patents at the facsimile number indicated below.		
Signature:	<i>Dorian Cartwright</i>	
Typed or Printed Name:	Dorian Cartwright	Dated: 1/19/06
Facsimile Number:	571-273-8300	

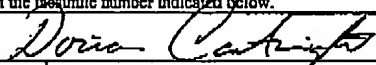
22347/08261/DOCS/1590033.1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Donald E. Steiss, et al.
APPLICATION NO.: 10/749,912
FILING DATE: December 30, 2003
TITLE: Instruction Memory Hierarchy for Embedded Processor
EXAMINER: Not yet known
GROUP ART UNIT: 2186
ATTY. DKT. NO.: 22347-08261

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Facsimile Number:	571-273-8300		

COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

STATUS REQUEST

SIR:

Our file for the subject application reveals that there has been no action on this application since the mailing of the Filing Receipt on May 4, 2004.

Please inform the undersigned, at the below stated address, of the status of this application.

Respectfully submitted,
DONALD STEISS, ET AL.

Dated: 1/19/06

By: 

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22347/08261/DOCS/1590031.1
DRAFT